

Amendments to the Claims:

This listing of claims will replace all prior versions and listing of claims in the application:

5 **Listing of Claims:**

Claim 1-10 (cancelled).

Claim 11 (previously presented): An active matrix display device comprising:

- 10 a plurality of scanning lines;
- a plurality of data lines;
- a plurality of pixels, each of the pixels electrically connected to one
 corresponding scanning line and one corresponding data line, each
 of the pixels comprising:
 - 15 a single first active device having a first end electrically
 connected to the corresponding scanning line, a second
 end electrically connected to the corresponding data line,
 and a third end;
 - 20 a plurality of active-type light emitting devices electrically
 connected in parallel with each other, each of the
 active-type light emitting devices being connected
 between a source of first potential and a source of second
 potential, each of the active-type light emitting devices
 comprising:
 - 25 a light emitting device electrically connected to the
 source of second potential; and
 - a second active device having a fourth end
 electrically connected to the third end, a fifth
 end electrically connected to the source of first
 potential, and a sixth end electrically connected

to the light emitting device, wherein the single
first active device switches each of the
active-type light emitting devices; and
a storage capacitor having a first electrode electrically connected to the third
5 end of the single first active device and the fourth end of the
active-type light emitting devices, and a second electrode
electrically connected to the source of first potential end.

Claim 12 (previously presented): The active matrix display device of claim 11,
10 wherein the single first active device is a first thin film transistor, and the
first end is a gate electrode of the first thin film transistor, the second end is
a drain electrode of the first thin film transistor, and the third end is a source
electrode of the first thin film transistor.

15 Claim 13 (previously presented): The active matrix display device of claim 11,
wherein the storage capacitor is electrically connected between the third end
of the single first active device and the source of first potential that is
utilized for supplying a constant potential.

20 Claim 14 (previously presented): The active matrix display device of claim 13,
wherein the source of first potential is utilized for supplying a constant
potential.

Claim 15 (original): The active matrix display device of claim 11, wherein each of the
25 second active devices comprises a second thin film transistor or a
complementary metal-oxide semiconductor (CMOS).

Claim 16 (original): The active matrix display device of claim 15, wherein the fourth
end is a gate electrode of the second thin film transistor, the fifth end is a
30 source electrode of the second thin film transistor, and the sixth end is a

drain electrode of the second thin film transistor.

5 Claim 17 (original): The active matrix display device of claim 11, wherein each of the
light emitting devices comprises an organic light emitting diode (OLED) or
a light emitting diode (LED).

10 Claim 18 (previously presented): The active matrix display device of claim 11,
wherein when an electrical shortage occurs in one of the active-type light
emitting devices of a pixel, the pixel displays an image via the other
active-type light emitting devices of the pixel.